

**What is claimed is:**

1        1.    A liquid crystal display, comprising:  
2        a power device;  
3        a display unit array;  
4        a scan driver, coupled to the power device,  
5                outputting a plurality of scan signals to the  
6                display unit array, wherein the scan driver  
7                sequentially outputs the scan signals to the  
8                display unit array in normal operation of the  
9                liquid crystal display, and the scan driver  
10              outputs an erase signal and all the scan  
11              signals during shutdown and power on processes  
12              of the liquid crystal display;  
13        a selection device having a first input terminal  
14              coupled to the power device, a first output  
15              terminal coupled to the scan driver, a second  
16              output terminal, and a first control terminal,  
17              wherein when the first control terminal  
18              receives the erase signal, and the selection  
19              device couples the first input terminal to the  
20              second output terminal; and  
21        a current limiting device, coupled between the  
22              second output terminal and the scan driver,  
23              limiting instantaneous current from the power  
24              device when the scan driver simultaneously  
25              outputs all the scan signals.

1        2.    The liquid crystal display as claimed in claim  
2        1, wherein the selection device comprises:

3           a first switch, having a second input terminal  
4 coupled to the first input terminal, a second control  
5 terminal coupled to the first control terminal, and a  
6 third output terminal coupled to the first output  
7 terminal, turned on and coupling the first input terminal  
8 to the first output terminal in normal operation; and

9           a second switch, having a third input terminal, a  
10 third control terminal coupled to the first terminal, and  
11 forth output terminal coupled to the second output  
12 terminal, turned on according to the erase signal and  
13 coupling the first input terminal to the second output  
14 terminal during shutdown and power on processes.

1           3. The liquid crystal display as claimed in claim  
2 2, wherein the first and second switches are MOS  
3 transistors.

1           4. The liquid crystal display as claimed in claim  
2 3, wherein the erase signal is at a low voltage level.

1           5. The liquid crystal display as claimed in claim  
2 4, wherein the first switch is an NMOS transistor and the  
3 second switch is a PMOS transistor.

1           6. The liquid crystal display as claimed in claim  
2 3, wherein the erase signal is at a high voltage level.

1           7. The liquid crystal display as claimed in claim  
2 6, wherein the first switch is an PMOS transistor and the  
3 second switch is a PNMOS transistor.

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- 1        8.    The liquid crystal display as claimed in claim
- 2    1, wherein the current limiting device is a resistor.